

Patent Abstracts of Japan

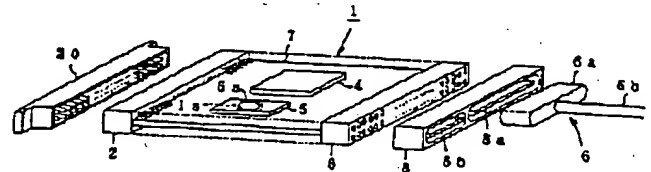
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TITLE : CARD TYPE INTERFACE EQUIPMENT



ABSTRACT : PROBLEM TO BE SOLVED: To provide one kind of interface equipment which enables connections with various communication environments by itself.

SOLUTION: After data for programming a logic circuit serving as an interface function for communication environments wherein a personal computer is connected in an FPGA (field programmable gate array) 4 are written in an EPROM (erasable read-only memory) 5 by a ROM writer, etc., through the data write connector 8b of an adapter 8, a PC card connector 2 is fitted in a slot 20 and a communication connector 8a and a communication environment to be used are connected by an adapter cable 6. Then when the FPGA 4 is supplied with electric power from the slot 20, an internal logic circuit is constituted according to the data stored in the EPROM 5, and consequently data transmission to and reception from a personal computer or peripheral equipment at a connection destination are controlled based on the interface of the connected communication environment.

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